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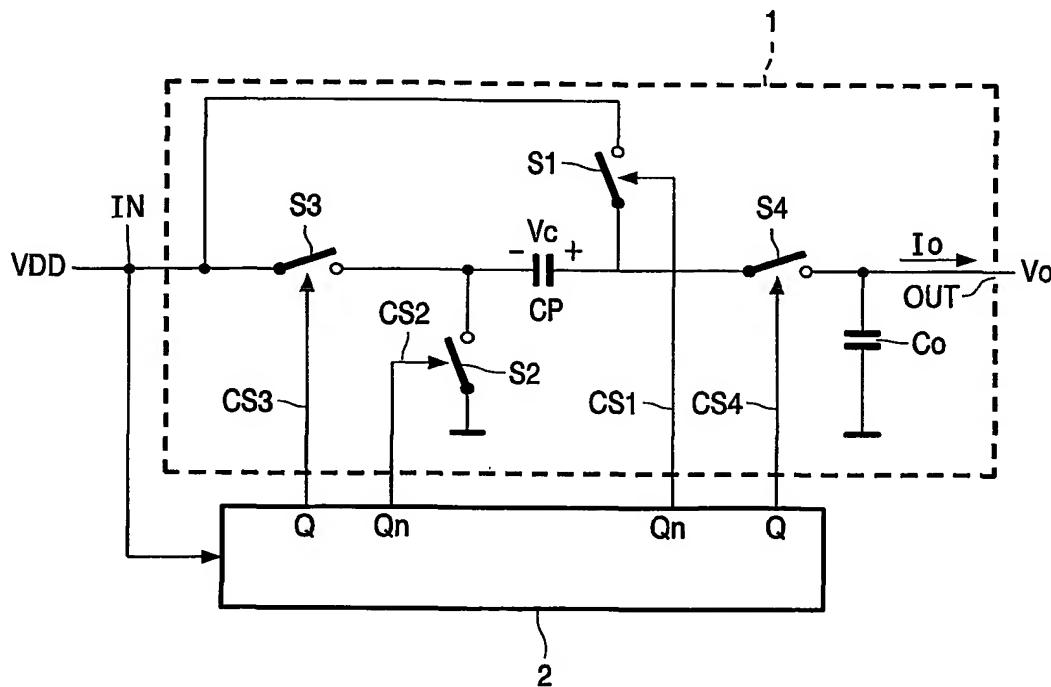
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(54) Title: A CHARGE PUMP



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(57) Abstract: A charge pump comprises a single voltage multiplier stage (1) which converts an input voltage (VDD) into an output voltage (V₀) under control of a clock signal (Q, Q_n; CLKO). An oscillator (2) receives the input voltage (VDD) to generate the clock signal (Q, Q_n; CLKO) having a repetition period (Tr₁, Tr₂) which is substantially proportional to a squared input voltage (VDD²).



FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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